Attorney's Docket No.: 12732-092002 / US5564/5995D1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

icant: Shunpei Yamazaki et al.

Art Unit : 2811

Serial No.: 10/718,584

Examiner: Unknown

Filed

: November 24, 2003

Confirmation No.: 1684

Title

: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

MAIL STOP AMENDMENT Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the references listed on the attached form PTO-1449. In accordance with the PTO's waiver of 37 CFR 1.98 (a)(2)(iii), only copies of foreign patent documents and non-patent references are enclosed.

This statement is being filed before the receipt of a first Office Action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 4, 2004

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Substitute Form PTO 2449 (Modified)

U.S. Separtment of Commerce Referent and Trademark Office Information Disclosure Statement

Attorney's Docket No. 12732-092002

Application No. 10/718,584

by Applicant (Use several sheets if necessary)

Applicant Shunpei Yamazaki et al. Filing Date

Group Art Unit

(37 CFR §1.98(b))

November 24, 2003

2811

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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	AB	3,791,883	02/12/1974	Takei et al.			
	AC	4,309,224	01/05/1982	Shibata			
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	AG	4,851,363	07/25/1989	Troxell et al.			
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Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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(Use several sheets if necessary) (37 CFR §1.98(b))		Filing Date November 24, 2003	Group Art Unit 2811	

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	ABB	5,773,327	06/30/1998	Yamazaki et al.			
	ACC	5,843,225	12/01/1998	Takayama et al.		,	
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_	AGG	US 2002/0164843 (with current claims)	11/07/2002	Yamazaki et al.			
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	AII	JP 04-011722	01/16/1992	Japan			Full	
	AJJ	JP 06-013610	01/21/1994	Japan			Abs	
	AKK	JP 06-267978	09/22/1994	Japan			Abs	
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	AMM	JP 60-105216	06/10/1985	Japan			Abs	

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	ANN	S. Caune et al.; "Combined CW Laser and Furnace Annealing of Amorphous Si and Ge in Contact with some Metals"; Applied Surface Science, vol. 36, pp. 597-604 (1989)				
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	APP	Kenji Sera et al.; "High-Performance TFT's Fabricated by XeCl Excimer Laser Annealing of Hydrogenated Amorphous-Silicon Film"; IEEE Transactions On Electron Device, vol. 36, no. 12; pp. 2868-2872 (December 1989)				
	AQQ	L. Hultman et al.; "Crystallization of Amorphous Silicon During Thin-film Gold Reaction"; J. Appl. Phys., vol. 62, no. 9; pp. 3647-3655 (November 1987)				
	ARR	Yunosuke Kawazu et al.; "Low-Temperature Crystallization of Hydrogenated Amorphous Silicon Induced by Nickel Silicide Formation"; <u>Japanese Journal of Applied Physics</u> , vol. 29, no. 12; pp. 2698-2704 (1990)				

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	Other Documents (include Author, Title, Date, and Place of Publication)					
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Initial	ID	Document				
	ASS	M. Fuse et al.; "Performance of Poly-Si Thin Film Transistors Fabricated by Excimer-Laser Annealing of SiH ₄ - and Si ₂ H ₆ - Source Low Pressure Vapor Deposited a-Si Films with or without Solid-Phase Crystallization"; Solid State Phenomena, vols. 37-38; pp. 565-570 (1994)				
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	AUU	Sorab Ghandhi.; "Thermal Oxidation of Gallium Arsenide"; VLSI Fabrication Principles - Silicon and Gallium Arsenide; 2 nd Edition, pp. 484, 485, 533-535				
	AVV	S.M. Sze; <u>VLSI Technology</u> ; 2 nd Edition, pp. 85-86, 266-267 (1988)				
_	AWW	Hongyong Zhang et al.; "KrF Excimer Laser Annealed TFT with Very High Field-Effect Mobility of 329 cm ² /v·s"; <u>IEEE Electron Device Letters</u> ; vol. 13, no. 5; pp. 297-299 (May 1992)				

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